REMARKS

Applicants thank the Examiner for the total consideration given the present application. Claims 1-6 remain pending. Claims 1-6 are independent. Favorable reconsideration and allowance of the present application are respectfully requested in view of the following remarks.

ALLOWABLE SUBJECT MATTER

Applicants appreciate that claims 1, 2, and 5-6 are allowed.

PRIOR ART REJECTION

Claims 3 and 4 stand rejected under 35 U.S.C. § 102(e) as allegedly being anticipated by Kato et al. (U.S. Patent No. 6,420,245)[hereinafter "Kato"]. Applicants respectfully traverse this rejection.

For a Section 102 rejection to be proper, the cited reference must teach or suggest each and every claimed element. *See M.P.E.P. 2131; M.P.E.P. 706.02.* Thus, if the cited reference fails to teach or suggest one or more elements, then the rejection is improper and must be withdrawn.

Independent claim 3 recites an apparatus, which includes a controller for generating a control signal for controlling a cycle of an output horizontal synchronizing signal used for reading out the accumulated image data from the memory, according to image size information of the input image signals, cycle information of the input horizontal synchronizing signals, and cycle information of the second clock. Claim 4 also recites this subject matter as a method step.

It is respectfully submitted that Kato fails to teach or suggest the above-identified *controller* or *controlling step*.

As previously submitted, Kato discloses in Fig. 4, an A/D converter 91 for converting inputted analog video signals (RGB) into digital video signal data, a buffer memory 92 for storing the digital data, e.g., video data of one horizontal line of the image temporarily. Kato further discloses a D/A converter 93 for converting the digital video signals into the analog video signals, a timing generator 94 for controlling the operation timings of the aforementioned A/D converter 91, buffer memory 92 and D/A converter 93. Kato continues to disclose a PLL (Phase

Locked Loop) circuit 95 for generating clocks, as synchronized with the inputted horizontal synchronizing signals, of the timing generator 94. (*Col. 7, lines 13-25.*)

More specifically, Kato's timing generator 94 has a control signal generator 99, a read clocks generator 97, and a control mode discriminator 98. (*Fig. 4.*)

The control mode discriminator 98 of Kato discriminates the operation mode in accordance with the up/down function switching input control signals for switching the operations of the timing generator 94. The control signal generator 99 generates the individual read timings in accordance with the information of the mode discriminator 98. The read clocks generator 97 generates read clocks based on the control signals, which is generated by the control signal generator 99. (*Col. 7, lines 26-35.*) Further, the read clocks generator 97 outputs the output horizontal synchronizing signal at the frequency which is determined by the operation mode. (*Fig. 4, col. 11, lines 1-6, 31-35.*)

In this way, the timing generator 94 of Kato switches a cycle of the output horizontal synchronizing signal in accordance with the information of the mode discriminator 98. The timing generator 94, however, ***does not*** use "***image size information of the input image signals, cycle information of the input horizontal synchronizing signals, and cycle information of the second clock***".

As mentioned earlier, the controller as recited in claim 3 generates a control signal for controlling a cycle of an output horizontal synchronizing signal used for reading out the accumulated image data from the memory, according to image size information of the input image signals, cycle information of the input horizontal synchronizing signals, and cycle information of the second clock. Thus, by utilizing the controller (claim 3) or controlling step (claim 4), the image display device or method enables proper scaling processing of image at arbitrary conversion ratios. Therefore, the timing generator 94 is distinguished from the claimed controller (claim 3) or controlling step (claim 4).

Therefore, for at least the above reasons, it is respectfully submitted that claims 3 and 4 are allowable over Kato.
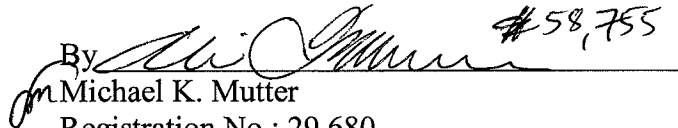
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CONCLUSION

In view of the above remarks, Applicants believe the pending application is in condition for allowance.

Should there be any outstanding matters that need to be resolved in the present application, the Examiner is respectfully requested to contact Ali M. Imam Reg. No. 58,755 at the telephone number of the undersigned below, to conduct an interview in an effort to expedite prosecution in connection with the present application.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37.C.F.R. §§1.16 or 1.17; particularly, extension of time fees.

Dated: July 13, 2009

Respectfully submitted,

By _____ #58,755

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